

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**INFORMATION DISCLOSURE STATEMENT**

|   |   |                            |
|---|---|----------------------------|
| In re Patent Application of:            | ) |                            |
|   | ) | Group Art Unit: 2822       |
| Toshibaru Yanagida                      | ) |                            |
|   | ) | Examiner: Not Yet Assigned |
| Application No. 09/385,959              | ) |                            |
|   | ) |                            |
| Filed: August 30, 1999                  | ) |                            |
|   | ) |                            |
| For: <b>SEMICONDUCTOR APPARATUS AND</b> | ) |                            |
| <b>PROCESS OF PRODUCTION THEREOF</b>    | ) |                            |
|   | ) |                            |

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Commissioner For Patents  
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Alexandria, VA 22313-1450

S I R:

In accordance with the provisions of 37 C.F.R. § 1.56, Applicants request that citation and examination of the references identified on the attached PTO-SB08A form, in accordance with 37 C.F.R. §1.98, be made during the course of examination of the above-referenced application for United States Letters Patent. These references were cited in an Office Action issued on April 30, 2008, in connection with Japanese application no. JP11-234661. Copies of the foreign references and machine translations are attached as is a copy of the Japanese office action.

Respectfully submitted,

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